

Instructions for Preparation and Submission of Papers for the Proceedings of 13th International Conference on Computer Science and Information Technologies (CSIT 2021)

First-Name Second-Name
University Name
City, Country
email: email1@email.com

First-Name Second-Name
University Name
City, Country
email: email2@email.com

First-Name Second-Name
University Name
City, Country
email: email3@email.com

Abstract—This paper provides the instructions for the preparation of papers for submission to CSIT 2021 and relevant style file that produced this page.

Keywords— Computer science, informatics, information technology.

I. INTRODUCTION

The 2021 Computer Science and Information Technologies Conference will be held at Yerevan, Armenia, 27 September – 1 October, 2021.

The major topics include, but are not limited to the following:

- Algorithms, Automata and Logic
- Artificial Intelligence and Machine Learning
- Cloud and High Performance Computing
- Cognitive Algorithms and Models
- Cryptography and Cyber-security
- Design and Test
- Discrete Mathematics and Combinatorial Analysis
- Information Technologies Applications
- Information Theory and Coding Theory
- Distributed Systems
- Big Data and IoT
- Pattern Recognition and Image Processing
- Telecommunication and Networking

II. PREPARATION OF THE PAPER

Authors are encouraged to use LaTeX to prepare their extended abstracts, using the style file and example on the conference web-site. Authors using other means to prepare their abstracts should attempt to duplicate the style of the example as closely as possible.

Authors are invited to submit papers in PDF format (template available on the Conference website) by submission system. Accepted papers will be published (up to 4 pages in length) in the Conference Proceedings.

III. DETAILS

A. Contact adress

National Academy of Sciences of Armenia, 24,
M.Baghramian ave, Yerevan, 0019, Armenia
Institute for Informatics and Automation Problems,
1, P. Sevak str., Yerevan, 0014, Armenia
E-mail: csit@sci.am
Phone: +(374 10) 527090, 282030, 282050
Fax: +(374 10) 569281

B. Important Dates

Workshop Proposals deadline: June 15, 2021
Papers submission deadline: June 30, 2021
Notification of acceptance: July 31, 2021
Final papers due: August 31, 2021
Deadline for early registration: August 31, 2021

Theorem 1: This is the sample theorem...

Proof: The proof of the theorem. \square

IV. EXAMPLES OF REFERENCE ITEMS

The list of references is given in the order of citing. Examples of reference items of different categories shown in the References section include:

- example of a book in [1]
- example of a book in a series in [2]
- example of a journal article in [3]
- example of a conference paper in [4]
- example of a patent in [5]
- example of a website in [6]
- example of a web page in [7]
- example of a databook as a manual in [8]
- example of a datasheet in [9]
- example of a master's thesis in [10]
- example of a technical report in [11]
- example of a standard in [12]

ACKNOWLEDGMENT

The authors would like to thank various sponsors for supporting their research.

REFERENCES

- [1] T. M. Cover and J. A. Thomas, *Elements of Information Theory*, Second Edition. Wiley, New York, 2006.
- [2] J. Breckling, Ed., *The Analysis of Directional Time Series: Applications to Wind Speed and Direction*, series, Lecture Notes in Statistics, Berlin, Germany: Springer, vol. 61, 1989
- [3] C. C. Leang and D. H. Johnson, "On the asymptotics of M-hypothesis Bayesian detection", *IEEE Transactions on Information Theory*, vol. 43, no. 1, pp. 280–282, 1997.
- [4] E. Haroutunian, M. Haroutunian and N. Afshar, "Random coding bound for E-capacity of the wiretap channel", *Proceedings of International Conference Computer Science and Information Technologies*, Yerevan, Armenia, pp. 121–124, 2011.
- [5] R. E. Sorace, V. S. Reinhardt and S. A. Vaughn, "High-speed digital-to-RF converter", U.S. Patent 5 668 842, Sept. 16, 1997.
- [6] (2002) The IEEE website. [Online]. Available: <http://www.ieee.org/>
- [7] M. Shell, (2002) IEEEtran homepage on CTAN. [Online]. Available: <http://www.ctan.org/tex-archive/macros/latex/contrib/supported/IEEEtran>
- [8] FLEXChip Signal Processor (MC68175/D), Motorola, 1996.
- [9] "PDCA12-70 data sheet", Opto Speed SA, Mezzovico, Switzerland.
- [10] A. Karnik, "Performance of TCP congestion control with rate feedback: TCP/ABR and rate adaptive TCP/IP", M. Eng. thesis, Indian Institute of Science, Bangalore, India, 1999.
- [11] J. Padhye, V. Firoiu and D. Towsley, "A stochastic model of TCP Reno congestion avoidance and control," Univ. of Massachusetts, Amherst, MA, CMPSCI Tech. Rep. 99-02, 1999.
- [12] *Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) Specification*, IEEE Std. 802.11, 1997.